ZETTLER DISPLAYS

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

	CUSTOMER APP	PROVAL	
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※ P .	ART NO.: <u>ATM3224H</u>	SERIES SPEC	VER1.0
APPROVAL		COMPANY CHOP	
CUSTOMER			
COMMENTS			

ZETTLER DISPLAYS ENGINEERING APPROVAL						
DESIGNED BY	CHECKED BY	APPROVED BY				

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1.0 GENERAL SPECS

ATM3224H series high contrast graphic module is an advanced product which adopts TFT and FPGA technology. It can perfectly replace the old 320*240 STN/FSTN graphic module with almost no change on hardware and software. Customer can easily get higher contrast, wider viewing angle, better high/low temperature performance and other features with least cost increasing.

I	
Display Format	320*240 DOTS
2. Power Supply LCM	5.0V/3.3V (Refer to P/N description in last page)
3. Overall Module Size	144.0mm(W) x 104.0mm(H) x max 12.1mm max(D) *
4. Viewing Area(W*H)	114.9mm(W) x 86.67mm(H)
5. Pixel Pitch (W*H)	0.3528mm(W) x 0.3528mm(H)
6. Active Area(W*H)	112.896mm(W) x 84.672mm(H) (5.6" Diagonal)
7. Driving Method	TFT
8. Controller IC	Not included, support RA8835 or compatible externally
9. Display Mode	Customized display mode(Refer to P/N description)
10. Backlight Options	White LED
11. Operating temperature	-20°C ~ 70°C
12. Storage temperature	-30°C ~ 80°C
13. RoHS	RoHS compliant

^{*} Refer to outline drawing

2.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Тур	Max	Unit
Input voltage	Vin	Vss-0.3		Vdd+0.3	V
Supply voltage for logic	Vdd- Vss	-0.3 -		5.5	V

3.0 ELECTRICAL CHARACTERISTICS

3.1 Electrical Characteristics Of LCM

Item	Symbol	Condition	Min	Тур	Max	Unit
Power Supply Voltage	Vdd	25°C		5.0 or 3.3	-	V
Power Supply Current	ldd	Vdd		TBD		mA
Input voltage (high)	Vih	Pins:(FRM/LOAD/CP/DISP	0.8Vdd		Vdd	V
Input voltage (low)	Vil	` D0~D3)	0		0.2Vdd	V

3.2 The Characteristics Of LED Backlight

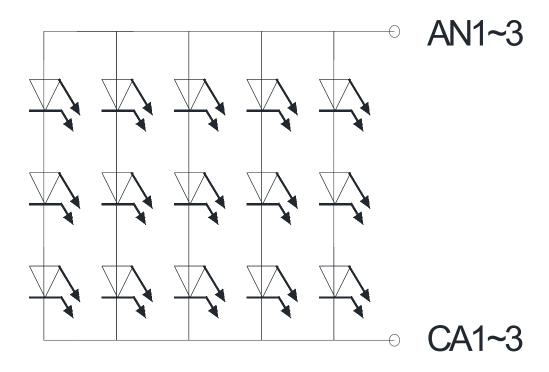
3.2.1 Electrical-Optical Characteristics Of LED Backlight (Ta=25°C)

Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage ⁽¹⁾⁽⁵⁾ V	f	If=100mA	8.4	9.3	10.5	V
Forward Current ⁽⁵⁾ If				100		mA
Luminance ⁽³⁾ Lv		If=100mA	150	200		cd/m²
Uniformity ⁽²⁾	∆ (Lv	min/Lvmax)%	70%	75%		-
Chroma coordinate	х	If=100mA	0.26 0.	31	0.36	-
Chiloma coordinate	y 0.2	II- TOOTIA	8	0.33	0.38	-
Lifetime ⁽⁴⁾ -		If=200mA	20000		-	Hours

NOTE:

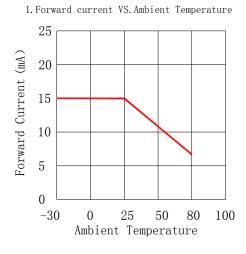
- (1) Forward voltage means voltage applied to Anode and cathode of backlight.
- (2) The luminan ce is the a verage value of 5 points, The me asurement in strument is BM-7 lumin ance colorimeter. The diameter of aperture is Φ 5mm.
 - (3) Luminance when LCM displaying white color.
- (4) Backlight lifetime means luminance value larger than half of the original after 2 0000 hours' continuous working.

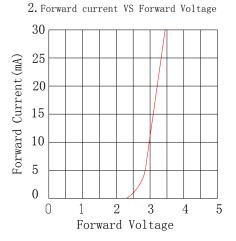
3.2.2 Backlight Control Circuit For LCM (3x5=15 pcs LED)



Note: AN1 to an3 are connected together inside backlight, CA1 to CA3 are connected together inside backlight. Forward current means the total current of the backlight, not the current of single string.

3.2.3 LED Characteristics Curves (for single led)

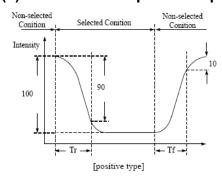


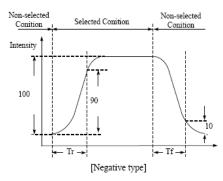


4.0 OPTICAL CHARACTERISTICS (Ta=25°C)

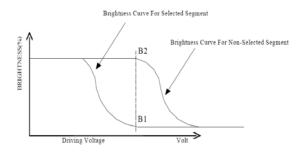
Item	Symbol	Condition	Min	Тур	Max	Unit
Viewing angle (Left - right)	θ2 C	r ≥ 2.0	-70	-	70	deg
Viewing angle (Up-down)	θ1 C	r ≥ 2.0	-50	-	70	deg
		FT:white,BG:black 40	0	500		-
Contrast Ratio	0	FT:black,BG:white 4	00	500		-
(θ1=0°, θ2=0°)	Cr	FT:white,BG:blue 48		60		-
		FT:black,BG:Y-G 3	20	400	-	-
Response time (rise)	Tr	θ1=0°, θ2=0° -		10	20	ms
Response time (fall)	Tf	θ1=0°, θ2=0° -		15	30	ms

(1). Definition of Optical Response Time



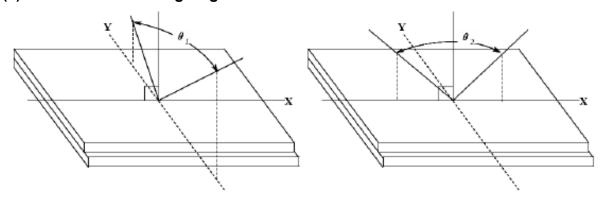


(2). Definition of Contrast Ratio



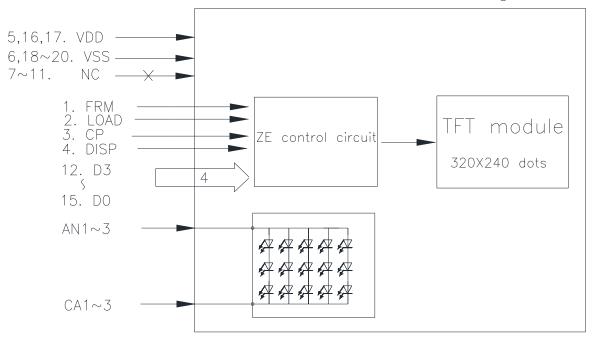
Cr= Brightness of Non-selected Segment(B2)
Brightness of selected Segment(B1)

(3). Definition of Viewing Angle $\theta 2$ and $\theta 1$



5.0 BLOCK DIAGRAM

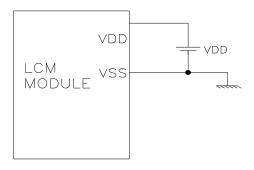
ATM3224H block diagram



6.0 PIN ASSIGNMENT

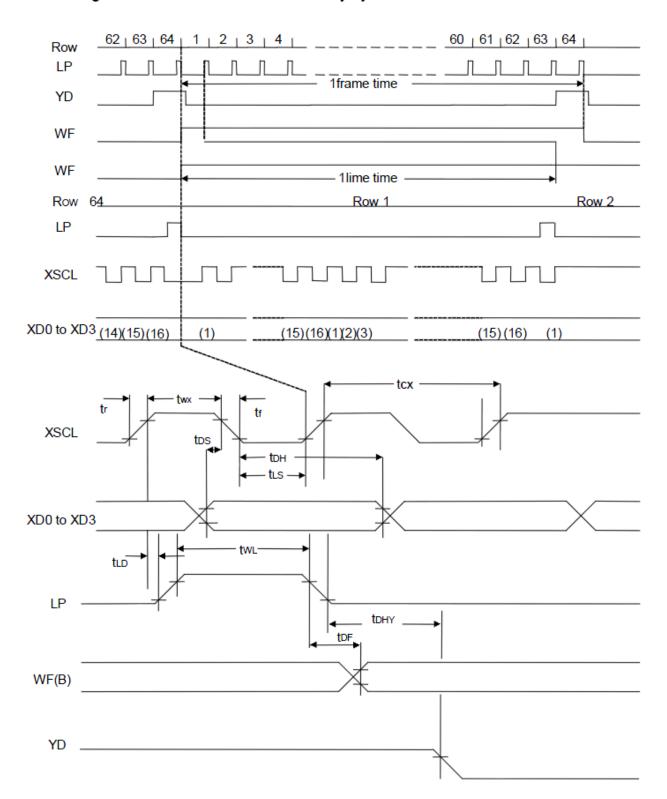
Pin No.	Symbol	Function	I/O
1	FRM	Synchronous signal for driving scanning line	I
2	LODAD	Data singal latch clock	I
3	СР	Data signal shift clock	I
4	DISP	Display control signal	I
5	VDD	Power supply for logic	P
6	VSS	Ground	P
7	NC		
8	NC		
9	NC	No connect	
10	NC		
11	NC		
12	D3		
13	D2	Display data	
14	D1	Display data	
15	D0		
16	VDD	Power supply for logic	P
17	VDD	Power supply for logic	P
18	VSS	Ground	P
19	VSS	Ground	P
20	VSS	Ground	P

7.0 POWER SUPPLY



8.0 TIMING CHARACTERISTICS

The following characteristics are for a 1/64 duty cycle.

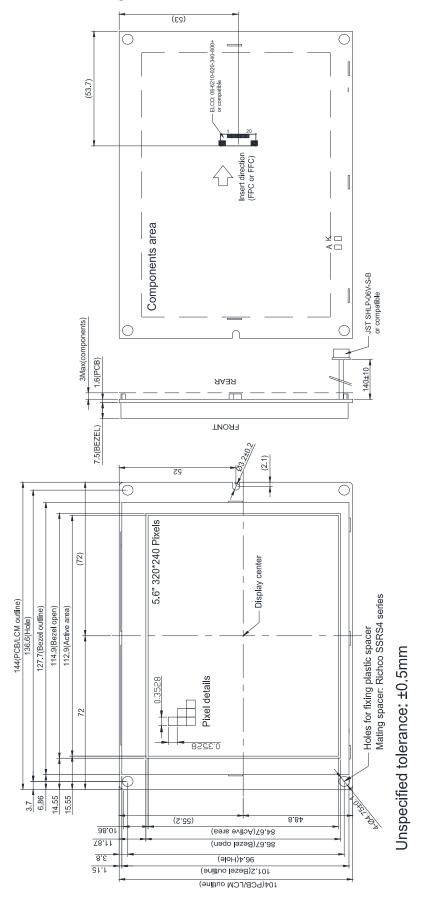


Ta = -20 to 75 °C

Signal	Symbol	Parameter	V _{DD} = 4.8	5 to 5.5V	V _{DD} = 2.7	7 to 4.5V	Unit	Condition
Signal	Gyllibol	T drameter	Min.	Max.	Min.	Max.	Oilit	Condition
	t _r	Rise time	_	30	_	40	ns	
	t _f	Fall time		30	_	40	ns	
XSCL	t _{cx}	Shift clock cycle time	2t _C	_	2t _C	_	ns	
	t _{wx}	XSCL clock pulse width	t _C - 20	_	t _C - 20	_	ns	
XD0 to	t _{DH}	X data hold time	2t _C - 50	_	2t _C - 50	_	ns	
XD3	t _{DS}	X data setup time	2t _c - 100	_	2t _c - 105	_	ns	CL =
	tus	Latch data setup time	t _C - 20	_	t _C - 20	_	ns	100 pF
LP	t _{WL}	LP pulse width	2t _C - 10	_	2t _C - 10	_	ns	
	t _{LD}	LP delay time from XSCL	t _C - 20	_	t _C - 20	_	ns	
WF	t _{DF}	Permitted WF delay	t _c - 20	t _c +30	t _C - 20	t _c +30	ns	
YD	t _{DHY}	Y data hold time	2t _c - 20		2t _C - 20		ns	

RA8835 timing. For other timing characteristics or detailed information, please refer to IC specification

9.0 MECHANICAL DIAGRAM



10.0 RELIABILITY TEST

NO	Te	est Item	Description	Test Condition	Remark
1		High temperature storage	Applying the high storage temperature Under normal humidity for a long time Check normal performance	80 °C 96hrs	
2		Low temperature storage	Applying the low storage temperature Under normal humidity for a long time Check normal performance	-30°C 96hrs	
3		High temperature Operation	Apply the electric stress(Voltage and current) Under high temperature for a long time	70 °C/80 °C 96hrs	Note1
4	Environmental Test	Low temperature Operation	Apply the electric stress Under low temperature for a long time	-20°C/-30 °C 96hrs	Note1 Note2
5	Test	High temperature/High Humidity Storage	Apply high temperature and high humidity storage for a long time	90% RH 40 ° C 96hrs	Note2
6		Temperature Cycle	Apply the low and high temperature cycle -30°C <> 25°C <> 80°C <> 25°C 30min 10min 30min 10min 1 cycle Check normal performance	-30°C/80°C 10 cycle	
7	Mechanical Test	Vibration test(Package state)	Applying vibration to product check normal performance	Freq:10~55~10Hz Amplitude:0.75mm 1cycle time:1min X.Y.Z every direction for 15 cycles	
8		Shock test(package state)	Applying shock to product check normal performance	Drop them through 70cm height to strike horizontal plane	
9	Other			•	

Remark

Note1:Normal operations condition (25°C±5°C).

Note2:Pay attention to keep dewdrops from the module during this test.

11.0 PRECAUTION FOR USING LCM

- 1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
- 2. As LCD panel is made of glass substrate, Dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
- 3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
- 4. If the LCD module is stored at below specified temperature, the LC material may freeze and be deteriorated. If it is stored at above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. Excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature range.
- 5. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if remained for a long time. Water vapor will cause corrosion of ITO electrodes.
- 6. If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft cloth. If it is not still clean enough, blow a breath on the surface and wipe again.
- 7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latch-up of driver LSIs and DC charge up to LCD panel.
- 8. M echanical Considerations
 - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
 - b) Do not tamper in any way with the tabs on the metal frame.
 - Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
 - d) Do not touch the elastomer connector; especially insert a backlight panel (for example, EL).
 - e) When mounting a LCM makes sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
 - f) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.
- 9. Sta tic Electricity
 - a) O perator

Ware the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Eq uipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth: 1x10⁸ ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) F loor

Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth: 1x10⁸ ohm).

d) Hu midity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept betweens 45%RH ~60%RH.

e) T ransportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

f) Solderi ng

Solder only to the I/O terminals. Use only soldering irons with proper grounding and no leakage.

Soldering temperature: 280°C ± 10°C

Soldering time: 3 to 4 sec.

Use eutectic solder with resin flux fill.

If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards

g) O thers

The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

Static eliminator should also be installed to the workbench to prevent LCD module from static charge. 10. Operation

- a) Driving voltage should be kept within specified range; excess voltage shortens display life.
- b) Response time increases with decrease in temperature.
- c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".
- 11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
- 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
- 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
- 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
- 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.
- 16. The brightness of LCD module may be affected by the routing of CCFL cables due to leakage to the chassis

through coupling effect. The inverter circuit needs to be designed taking the level of leakage current into consideration. Thorough evaluation is needed for LCD module and inverter built into its host equipment to ensure specified brightness.

Appendix: P/N description

ATM3224H-FLW-FBW-MC

C: Add CRC Blank: Without CRC

M: 3.3V logic power supply Blank: 5.0V logic power supply

TW: Viewing Direction is 12 o'clock RW: Viewing Direction is 3 o'clock BW: Viewing Direction is 6 o'clock LW: Viewing Direction is 9 o'clock extended working temperature

NLW-F: Black background, white font NLW-B: Blue background, white font FLW-F: White background, black font

FL-Y: Yellow-green background, black font

FLW-X: optional background/font color via switch on board

ATM3224H: ATM3224H series